

U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

SCANNED

TR3 Q.A. Am

APPLICATION NO.

CONT/PRIOR

CLASS

SUBCLASS

ART UNIT

EXAMINER

09/964645

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SCHECHTER

APPLICANTS

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THE

Thin film transistor array substrate for liquid crystal display

PTO-2040
12/99

ISSUING CLASSIFICATION

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